

AMD Selects NEW JTAG Tool to Raise the Bar on Quality

Austin, Texas – March 24th, 2006 – SiliconAid Solutions, Inc. announced today that AMD (NYSE:AMD) has selected the SiliconAid's JTV (JTAG verification) tool.

The JTV tool provides a robust verification environment to ensure that your verilog design with JTAG and your BSDL (Boundary Scan Description Language) are fully consistent and comply with the IEEE 1149.1 and 1149.6 standards.

"SiliconAid's JTV JTAG tool has proven to be an extremely thorough and easy to use tool that provides excellent feedback. The transition was seamless and enabled us to continue important functionalities with virtually no interruption. As a result, we have a strong process driven by a great tool." - Tim Wood, AMD Fellow

Complementary to an automated or custom JTAG design flow, JTV gives a quick, easy, and independent way to make sure your design is correct. JTV can output fault-simulated production test patterns and parametric tests. JTV is unique in its ability to verify that the chip design is JTAG-compliant and that the BSDL file accurately describes your JTAG design.

About SiliconAid Solutions : (www.siliconaid.com) SiliconAid Solutions, Inc. is headquartered in Austin, Texas and has been doing design-for-test consulting world-wide since 2001. The SiliconAid software group is led by Chief Scientist Bill Bruce, a former senior member of the technical staff at Motorola who was the principal developer of the internal Motorola JTAG solution. SiliconAid has developed a full suite of tools for JTAG creation, verification, and debug. SiliconAid Solutions, Inc. is a privately held Texas corporation.